

56-BYTES ALIGNED ATM CELL

ATM PAYLOAD (BYTE 1 THRU 47)	ATM HEADER (5 BYTES)	
(BITE I TIMO 47)		
BYTE 48 PORT ID, PRIORITY LEVEL	BYTE 48	PORT ID, PRIORITY LEVEL

FIG. 7

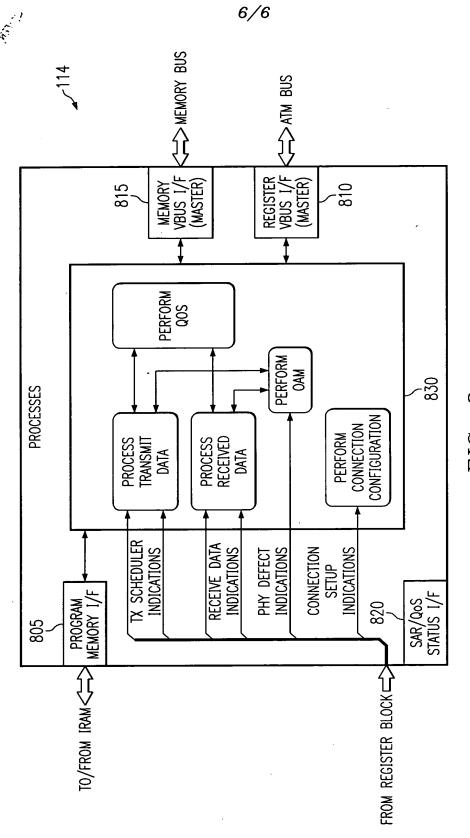


FIG. 8